液 晶 模 组 说 明 书

LCD Module Instructions

☑初定规格 Preliminary specification

☐正式规格 Official specifications

项目编号 Project No.	TFT-H070A18ZBINV7N40
产品描述 Product Description	TFT LCD Module 280(RGB) x 1424 Dots 7.0 Inch TFT LCD

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☐小批量试产 Trial Production ☐大批量生产 Mass Production

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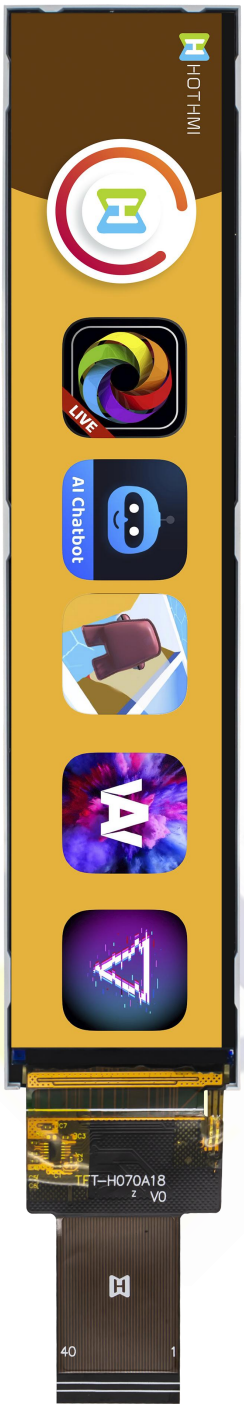
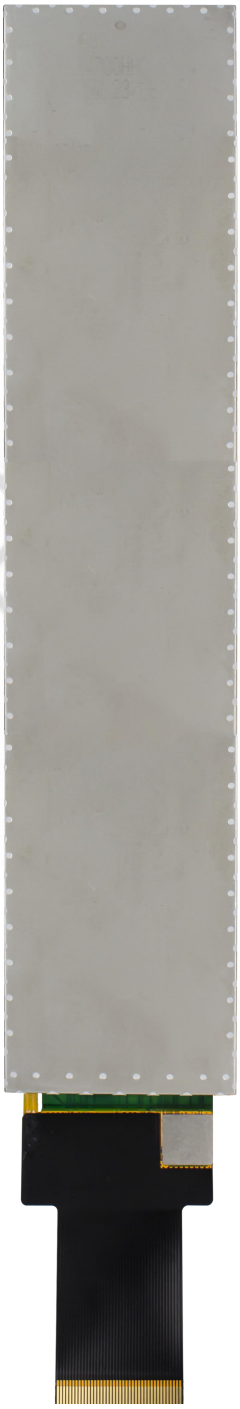


正面/FRONT



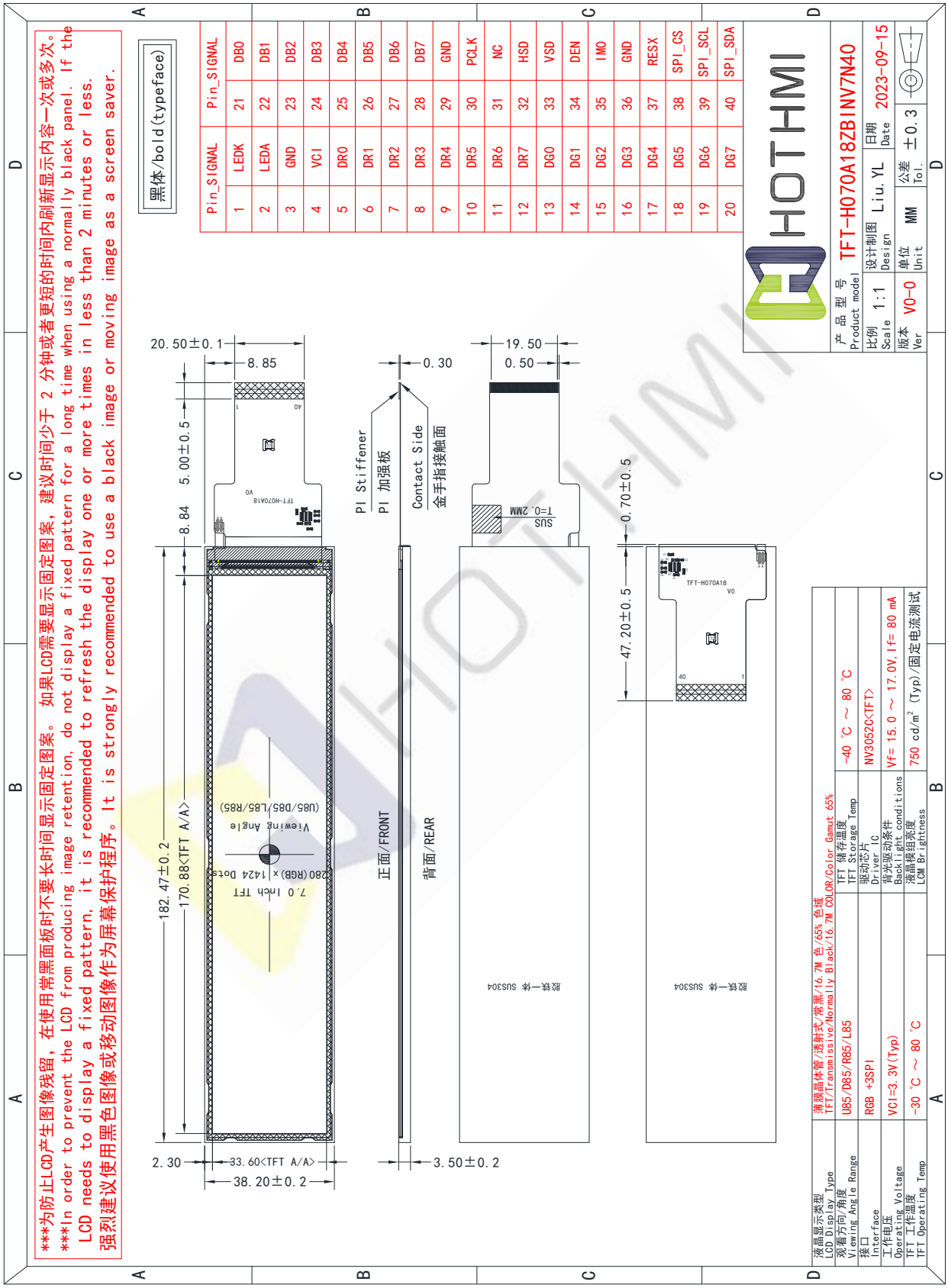
背面/REAR

一、基本特征 General Feature:

项目 Item	标准值 Standard Value			单位 Unit
显示尺寸 Display Size	7.0			英寸 Inch
分辨率 Number of Pixels	280(RGB) (H) x 1424 (V)			点 dots
显示区域 Active Area	33.60 (H) x170.88 (V)			毫米 mm
外形尺寸 Outline Dimension	38.20(H) x 182.47(V) x 3.50(D) <TFT>			毫米 mm
观看方向 Viewing Direction	上 85/下 85/左 85/右 85 U85/D85/L85/R85			-
端口 Interface	RGB + 3SPI			-
驱动芯片 Driver IC	NV3052C			-
驱动电压 Driver Condition	VCI=3.1 ～ 3.3V(Typ)			伏特 V
背光驱动条件 Backlight conditions	电压= 15.0 ～ 17.0V/电流= 80 mA 最大(恒流) Vf= 15.0 ～ 17.0V/If= 80 mA Max(constant current)			-
触摸屏 Touch Panel	☑不带触摸屏 Whitout TP	☐带电阻触摸屏 With RTP	☐带电容触摸屏 With CTP	-
电容触摸屏驱动芯片 CTP Driver IC	-			-
电容触摸屏驱动电压 CTP Driver Condition	-			伏特 V
TFT 液晶工作温度 TFT Operation Temp	-30 ～ +80			摄氏度 ℃
TFT 液晶储存温度 TFT Storage Temp	-40 ～ +80			摄氏度 ℃

注释 Note:

二、外形尺寸 Outline Dimensions



三、引脚说明 Pin Description

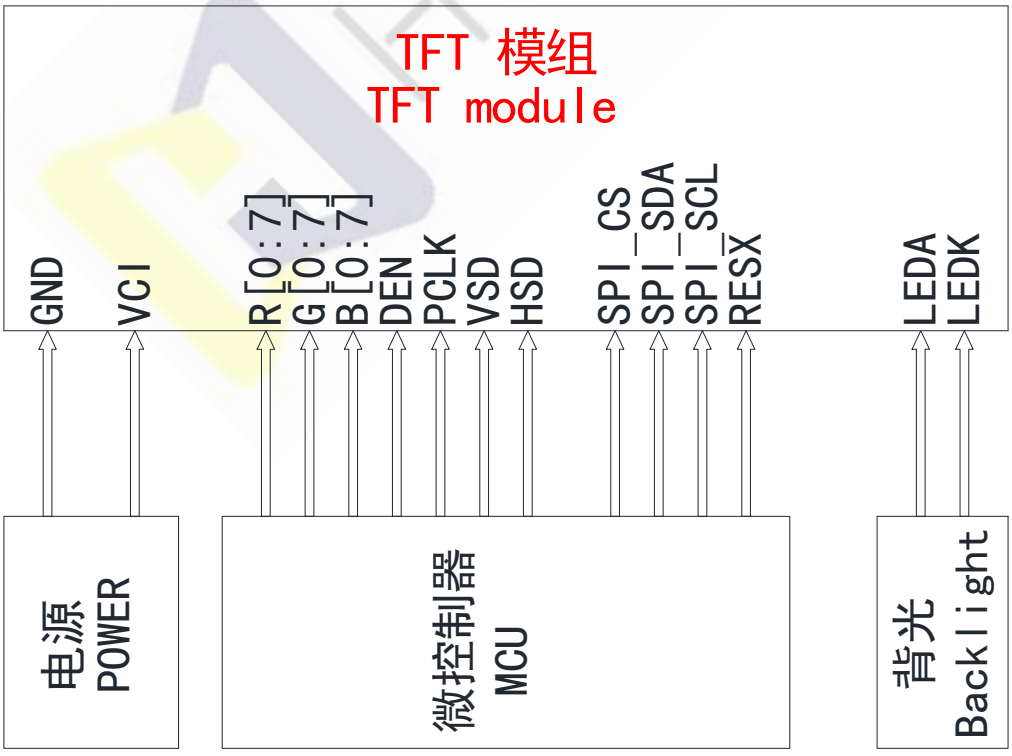
3.1 模组引脚说明 TFT Pin Description

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
1	LEDK	LED阴极 LED cathode
2	LEDA	LED阳极 LED anode
3	GND	电源地 Power supply ground
4	VCI	电源 Power supply
5～12	DR0～7	红色数据总线。请将不使用的引脚固定在GND。 Red data bus.Please fix the unused pin's to GND.
13～20	DG0～7	绿色数据总线。请将不使用的引脚固定在GND。 Green data bus.Please fix the unused pin's to GND.
21～28	DB0～7	蓝色数据总线。请将不使用的引脚固定在GND。 Blue data bus.Please fix the unused pin's to GND.
29	GND	电源地 Power supply ground
30	PCLK	像素时钟输入引脚 Pixel clock input pin
31	NC	不连接 Not connected
32	HSD	水平同步信号，默认为负极性。 Horizontal sync signal, default is negative polarity.
33	VSD	垂直同步信号，默认为负极性。 Vertical sync signal, default is negative polarity.
34	DEN	数据输入启用。当DE为 "H "时，显示访问被启用。 Data input enable. Display access is enabled when DE is "H".
35	NC	不连接 Not connected

- 接下页 -
- Continued on next page -

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
36	GND	电源地 Power supply ground
37	RESX	重置复位引脚 Reset pin
38	SPI_CS	芯片选择信号 A chip select signal
39	SPI_SCL	SPI接口串行时钟输入。 Serial clock input for SPI interface.
40	SPI_SDA	SPI接口串行数据输入/输出双向引脚。 Serial data input/output bidirectional pin for SPI Interface.
- 结束 - - END -		

3.2 接线说明 Wiring instructions



RGB 颜色格式
RGB Color Format

引脚名称 Pin name	24 位配置 24 bits configuration	18 位配置 18 bits configuration	16 位配置 16 bits configuration
DR7	DR7	GND	GND
DR6	DR6	GND	GND
DR5	DR5	DR5	GND
DR4	DR4	DR4	DR4
DR3	DR3	DR3	DR3
DR2	DR2	DR2	DR2
DR1	DR1	DR1	DR1
DR0	DR0	DR0	DR0
DG7	DG7	GND	GND
DG6	DG6	GND	GND
DG5	DG5	DG5	DG5
DG4	DG4	DG4	DG4
DG3	DG3	DG3	DG3
DG2	DG2	DG2	DG2
DG1	DG1	DG1	DG1
DG0	DG0	DG0	DG0
DB7	DB7	GND	GND
DB6	DB6	GND	GND
DB5	DB5	DB5	GND
DB4	DB4	DB4	DB4
DB3	DB3	DB3	DB3
DB2	DB2	DB2	DB2
DB1	DB1	DB1	DB1
DB0	DB0	DB0	DB0

四、电气特性 Electrical Characteristics

4-1 TFT模组工作条件 TFT LCD Module Operating Conditions

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
数字电源 Digital Power	VCI	-	-0.3	-	4.0	伏特 V
接口工作电压 IO Supply Voltage	IOVCC	-	-0.3	-	4.0	伏特 V
输入电压范围 Input Voltage Range	VIN	-	-0.3	-	IOVCC +0.3	伏特 V

注释 Note:

1. 超过上面列出的极限值可能会导致驱动IC永久损坏。 这些值仅用于测试。IC 应在芯片特性条件下正常运行。 如果不满足这些条件，IC 操作可能会出错，可靠性可能会下降。

That the exceeds the Limiting Value listed above it may cause the driver IC permanent damage. These values are for test only. IC should be operated under the Chip Characteristic conditions for normal operation. If these conditions are not met, IC operation may be error and the reliability may be deteriorated.

2. 参数在工作温度范围内有效，除非另有说明。 除非另有说明，所有电压均相对于 GND。

Parameters are valid over operating temperature range unless otherwise specified. All voltages are with respect to GND unless otherwise noted.

3. 确保 IOVCC、VCI 的电压电平始终符合正确的关系：2.6V ≦ IOVCC ≦ VCC ≦ 3.6V。

Insure the voltage levels of IOVCC, VCI, always matches the correct relation: 2.6V ≦ IOVCC ≦ VCC ≦ 3.6V.

4. VIN 应小于或等于 3.6V。 (VIN ≤ 3.6V)。

VIN should be less than or equal to 3.6V. (VIN ≦ 3.6V).

5. 面板显示质量取决于面板负载，在低温/高温下可能有不同的性能。

Panel display quality depends on panel loading, and it may have the different performance at low/high temperature.

4-2 TFT 面板工作条件 TFT Panel Operating Conditions

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
数字电源 Digital Power	VCI	-	3.1	3.3	3.6	伏特 V
接口工作电压 IO Supply Voltage	IOVCC	-	3.1	3.3	3.6	伏特 V
输入电压范围 Input Voltage Range	VIN	-	3.1	3.3	3.6	伏特 V
待机电流 Standby Current	Isc	No Load@ FR=60Hz	-	-	-	微安 uA
工作电流 Operation Current	Ioc		-	-	-	毫安 mA

4-3 电容触摸屏工作条件 CTP Operating Conditions

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
表面硬度 Surface hardness	-	铅笔	-	6H	-	硬度
透光率 Transmittance	-	-	80	-	-	百分比
数字电源 Digital Power	VDD	-	2.8	3.30	3.60	伏特 V
I/O 电源 I/O Power	IOVDD	-	2.8	3.30	3.60	伏特 V
工作温度 Operation Temperature	-	20%-90%RH;	-20	-	+70	摄氏度 ℃
储存温度 Storage Temperature	-		-30	-	+80	摄氏度 ℃

4-4 背光工作条件 LED back light specification

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
工作电压 Forward voltage	VF	恒流 constant current	15.0	16.0	17.0	伏特 V
工作电流 Forward current	IF		-	60	80	毫安 mA
亮度（带 LCD） Luminance(With LCD)	Lv	不带触摸 Without TP	-	550	750	坎德拉/平方米 cd/m^2
		带触摸 With TP	-	-	-	
LED 寿命 LED life time	Hr	Ta=25±3 ℃	20,000	30,000	-	小时 Hour

注释 Note:

1. LED 寿命（Hr）定义为在 Ta=25±3 ℃,上表所示的典型电压电流值条件下持续工作直至亮度低于 50% 的时间。

LED life time (Hr) can be defined as the time in which it continues to operate under the condition:Ta=25±3 ℃, typical IL value indicated in the above table until the brightness becomes less than 50%.

2. 以上结果是按 MTBF 計算方式预估判定的 LED 失效时间，实际测试 LED 在 Ta=25±3 ℃点亮 5000H，亮度衰减 8%.

The above results are estimated and judged by the MTBF calculation method of the LED failure time. The actual test LED is lit for 5000H at Ta=25±3 ℃, and the brightness decays by 8%.

五、液晶光学规格 TFT OPTICAL SPECIFICATION

5.1 概述 Overview

光学规格的测试应在暗室（环境亮度 1lux，温度=25 2℃）中使用亮度计系统（测角仪系统和TOPCON BM-5)设备进行测量,测试单元应位于大约 在 θ 和 Φ 等于 0 的视角下,距 LCD 表面 50cm 的距离。 显示面上测量点的中心应保持固定。 测量前背光应工作 30 分钟。

The test of Optical specifications shall be measured in a dark room (ambient luminance 1lux and temperature = 25 2℃) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0 . The center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement.

5.2 光学规格 Optical Specifications

参数 Parameter		标号 Symbol	条件 Condition	最小值 Min.	典型值 Typ.	最大值 Max.	单位 Unit	备注 Remark
视角范围 Viewing Angle Range	水平 Horizontal	Θ左/L	CR>10	-	85	-	Deg.	Note 1
		Θ右/R		-	85	-	Deg.	
	垂直 Vertical	Θ上/U		-	85	-	Deg.	
		Θ下/D		-	85	-	Deg.	
对比度 Contrast ratio		CR	Θ = 0°	900	1200	-	-	Note2
色域 Color Gamut		CG	CIE1931	60	65	-	%	
白色色度 White Chromaticity		Wx	Θ = 0°	-0.03	0.301	+0.03	-	Note4 (Based on C Light)
		Wy			0.329		-	
色彩还原 Reproduction of color	红 Red	Rx			-		-	
		Ry			-		-	
	绿 Green	Gx			-		-	
		Gy			-		-	
	蓝 Blue	Bx			-		-	
		By			-		-	
响应时间（上升 + 下降） Response Time (Rising + Falling)		Tr＋Tf	Θ = 0° Ta= 25℃	-	30	40	ms	Note5

注释 Note:

1. 视角是对比度大于10的角度。视角确定为相对于光轴的水平或3、9点钟方向和垂直或6、12点钟方向 垂直于 LCD 表面（见图 1）。

Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o’ clock direction and the vertical or 6, 12 o’ clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).

2. 对比度测量应在 θ= 0 的视角和 LCD 表面的中心进行。 亮度测量时，视场中的所有像素首先设置为白色，然后设置为暗（黑色）状态。（参见图 1）亮度对比度（CR）是通过数学定义的。

Contrast measurements shall be made at viewing angle of Θ= 0 and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state . (see FIGUR 1) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. 透射率是没有 APF 和没有 CG 的值。

Transmittance is the Value without APF and without CG.

4. 上表中规定的色度坐标应由所有像素首先测量的光谱数据计算为红色、绿色、蓝色和白色。 测量应在面板的中心进行。

The color chromaticity coordinates specified in the above table shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

5. 电光响应时间测量应如图 2 所示，通过打开和关闭“数据”输入信号来进行。 亮度从10%变化到90%所需的时间是Tr，90%到10%是Tf。

The electro-optical response time measurements shall be made as FIGURE 2 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Tf.

Figure1 Measurement Set Up

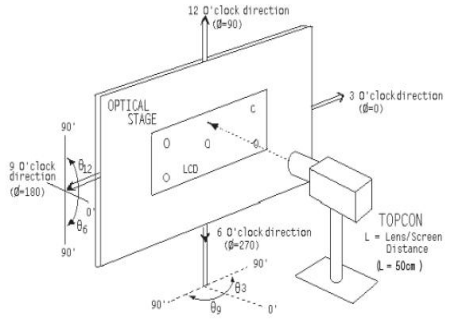


图 1

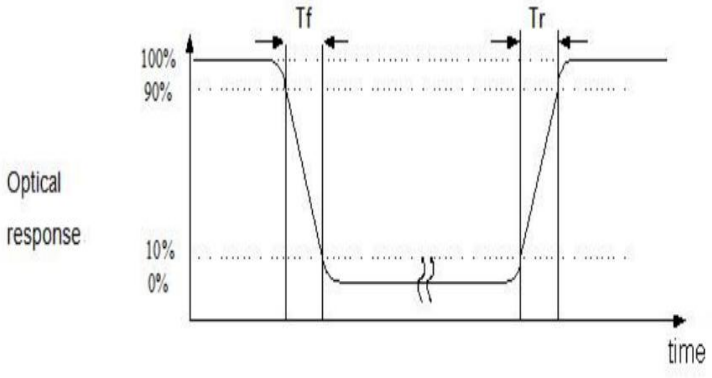


图 2

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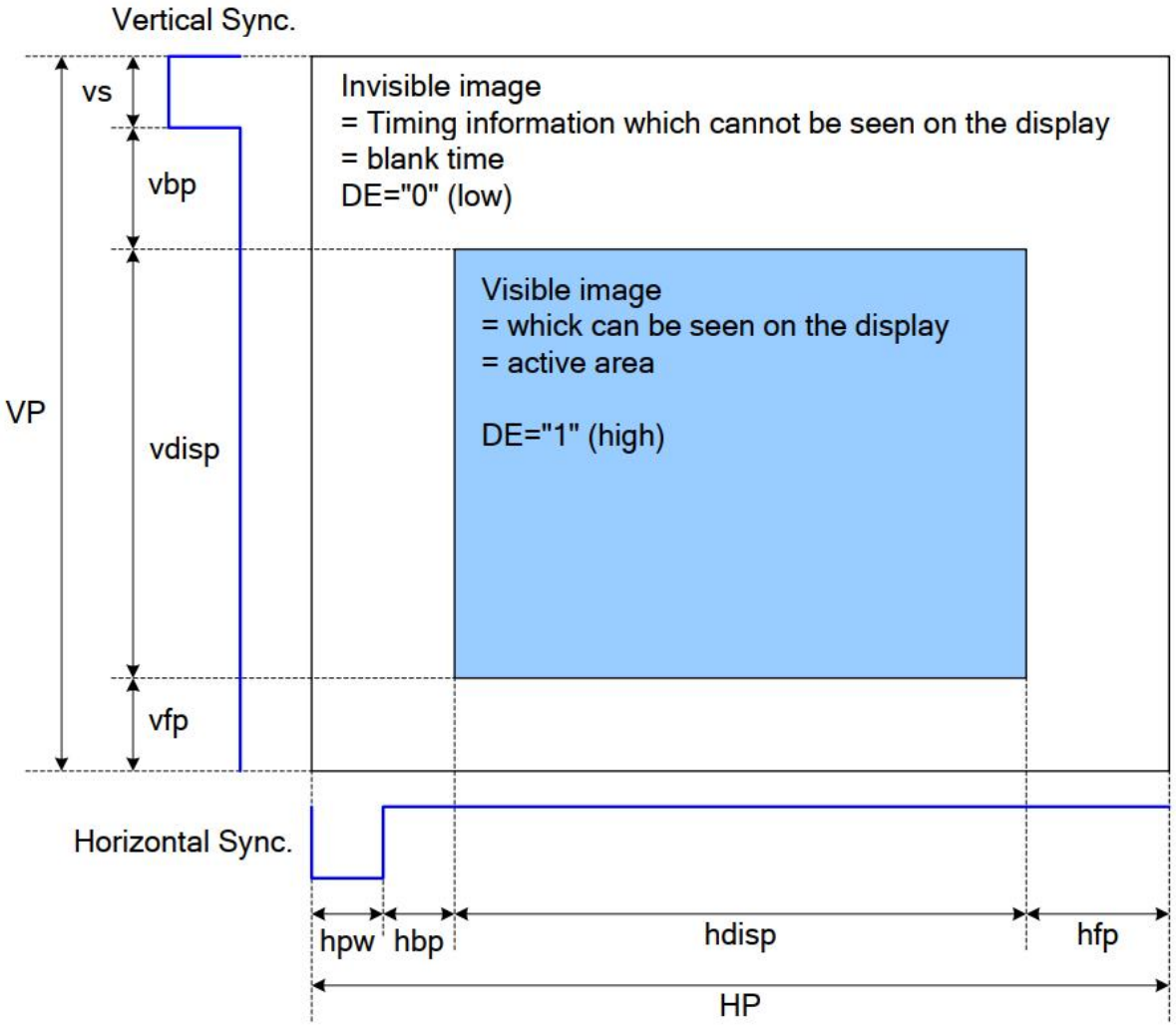
六、交流特性 AC characteristic

6.1. RGB 接口定义 RGB Interface Definition

通过 RGB 接口进行的显示操作与 VSYNC、HSYNC 和 DOTCLK 信号同步。使用窗口地址功能，数据只能在指定区域内写入，且功耗低。后门廊和前门廊用于设置 RGB 接口时序。

The display operation via the RGB interface is synchronized with the VSYNC, HSYNC, and DOTCLK signals. The data can be written only within the specified area with low power consumption by using window address function. The back porch and front porch are used to set the RGB interface timing.



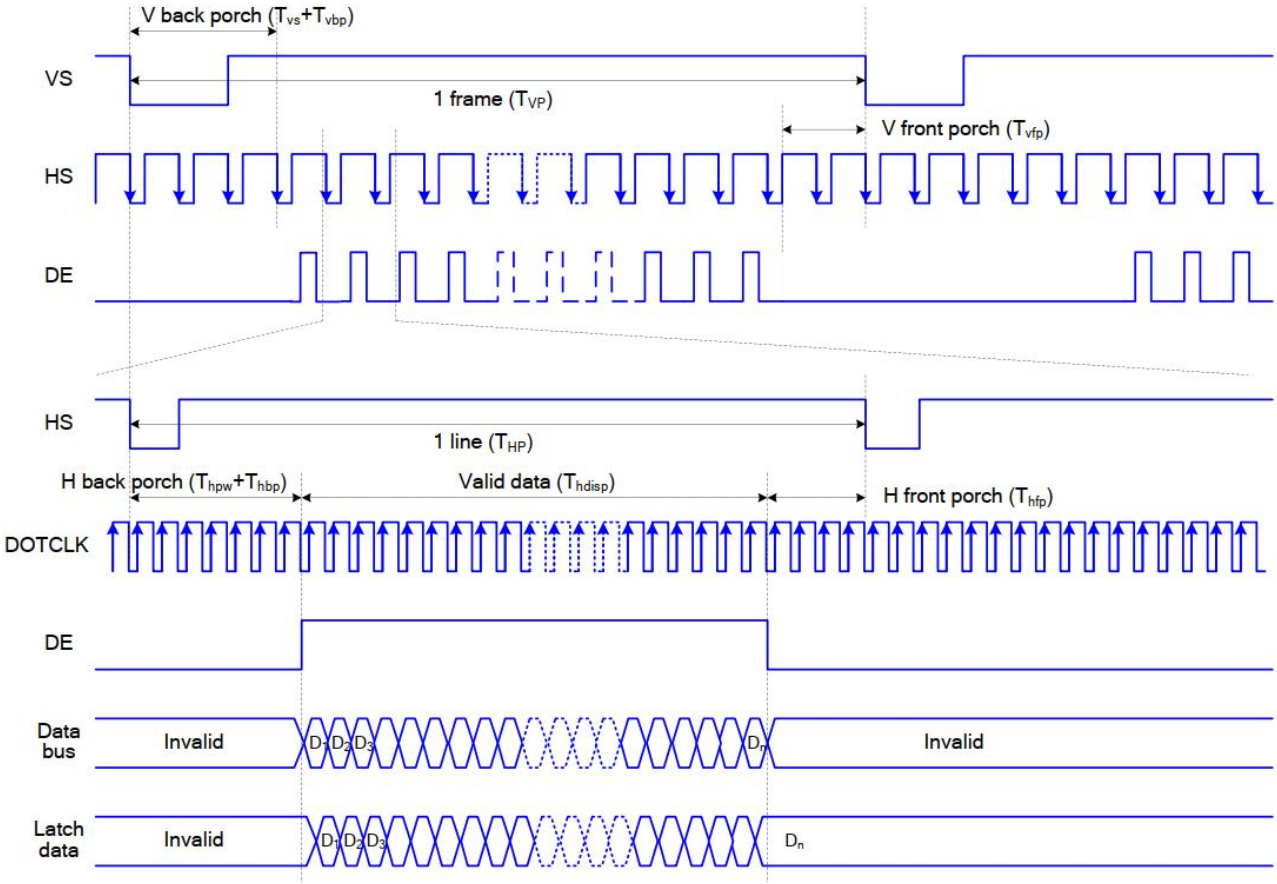
显示屏模组支持两种 RGB 接口：DE 模式和 HV 模式。使用客户命令 23h 选择 RGB 接口模式。

The display module supports two types of RGB interface: DE mode and HV mode. Use the Customer command 23h to select the RGB interface mode.

sync_mode[1:0]	RGB Mode
00	SYNC+DE mode
01	SYNC mode
10	DE mode
11	SYNC+DE mode

1）RGB 接口 DE 模式下的信号时序图

Timing Chart of Signals in RGB Interface DE Mode



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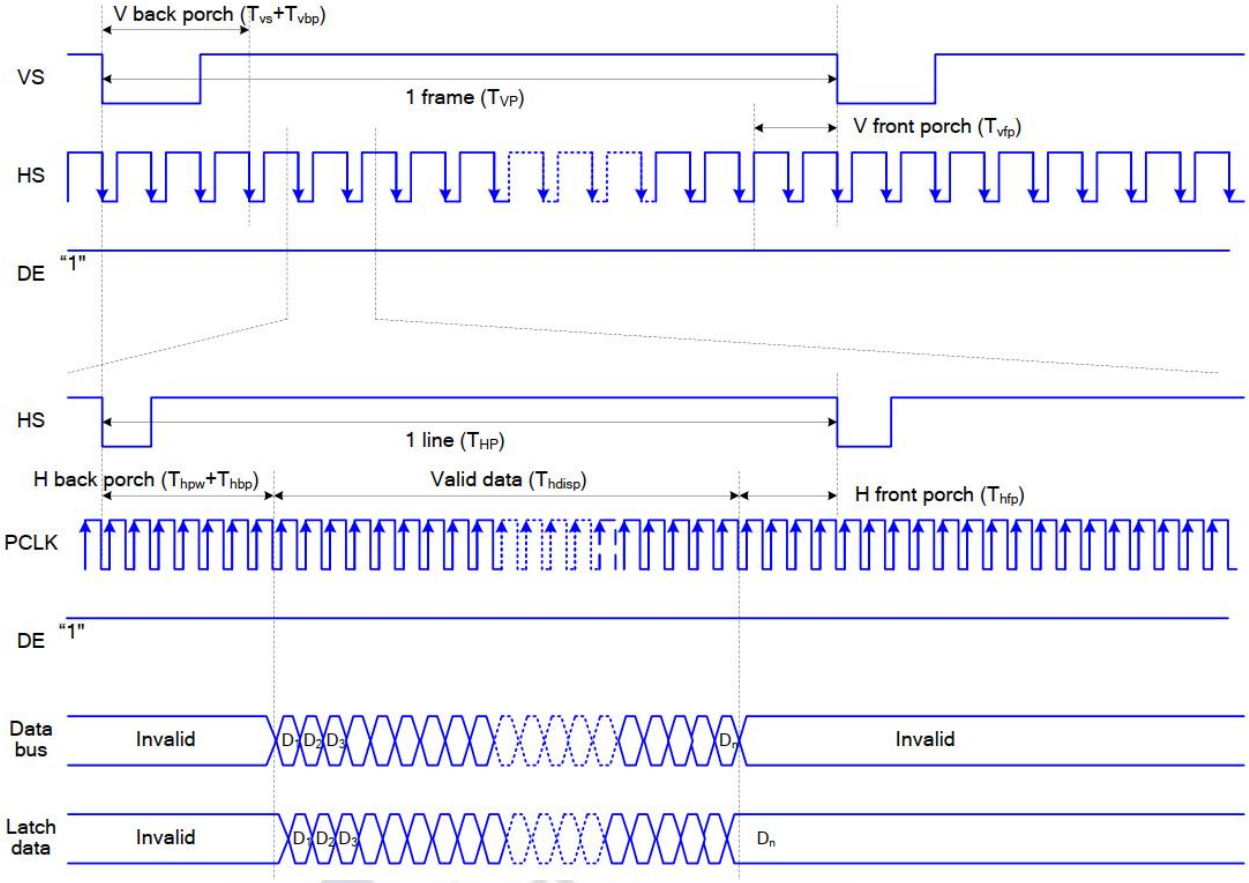
2）RGB 接口 SYNC 模式下的信号时序图

Timing Chart of Signals in RGB Interface SYNC Mode



(Resolution for 720/640 horizontal x 1280 vertical display with Frame-Rate of 60Hz)

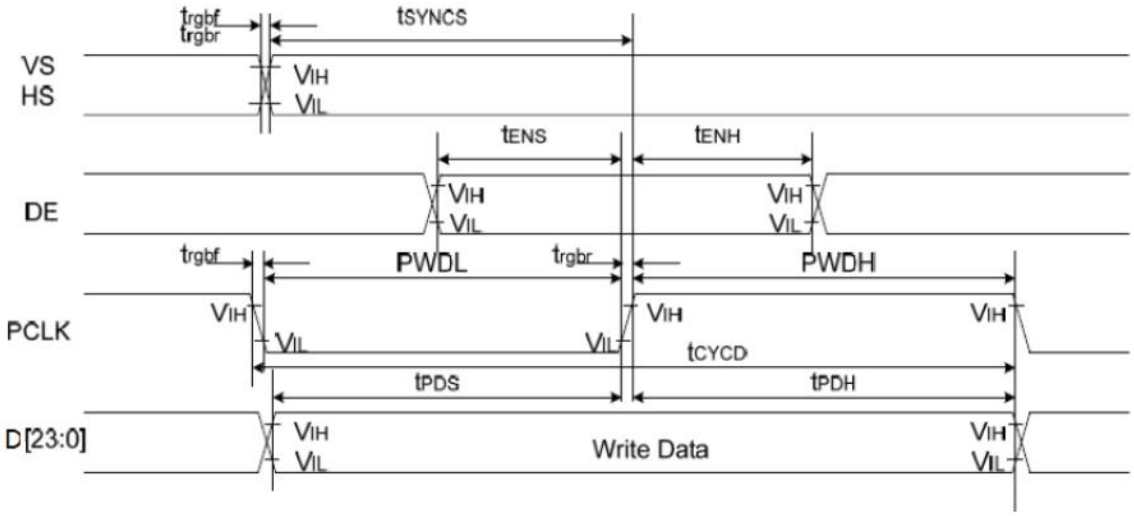
Parameters	Symbols	Min.	Typ	Max.	Unit
Horizontal Synchronization	hpw	-	2	-	PCLK
Horizontal Back Porch	hbp	-	42	-	PCLK
Horizontal Front Porch	hfp	-	44	-	PCLK
Hsync+ HBP+ HFP	-	-	88*Note1	-	PCLK
Horizontal Address (Display area)	hdisp	-	720	-	PCLK
Horizontal cycle	-	-	12.703	-	us
Vertical Synchronization	VS	-	2	-	Line
Vertical Back Porch	vbp	-	14	-	Line
Vertical Front Porch	vfp	-	16	-	Line
Vsync+ VBP+ VFP	-	-	32	-	Line
Vertical Address (Display area)	vdisp	-	1280	-	Line
Vertical cycle	-	-	16.66	16.181	ms
Frame-Rate	-	-	60	61.8	Hz

"-" means no limit.

Note : 1. If using Image Process Algorithm, Type value for H-blanking is minimum requirement.

3）并行 24/18/16 位 RGB 接口时序特性

Parallel 24/18/16-bit RGB Interface Timing Characteristics



Signal	Symbol	Parameter	min	max	Unit	Description
VS/HS	tSYNCS	VS/HS setup time	5	-	ns	24/18/16-bit bus RGB interface mode
	tSYNCH	VS/HS hold time	5	-	ns	
DE	tENS	DE setup time	5	-	ns	
	tENH	DE hold time	5	-	ns	
D[23:0]	tPOS	Data setup time	5	-	ns	
	tPDH	Data hold time	5	-	ns	
PCLK	PWDH	PCLK high-level period	13	-	ns	
	PWDL	PCLK low-level period	13	-	ns	
	tCYCD	PCLK cycle time	28	-	ns	
	trgbr,trgbf	PCLK,HS,VS rise/fall time	-	15	ns	

Note 1: IOVCC=1.65 to 3.6V, VCI=2.5 to 6V, VSSA=VSS=0V, Ta=-30 to 85℃

6.2. 串行接口特性 Serial Interface Characteristics

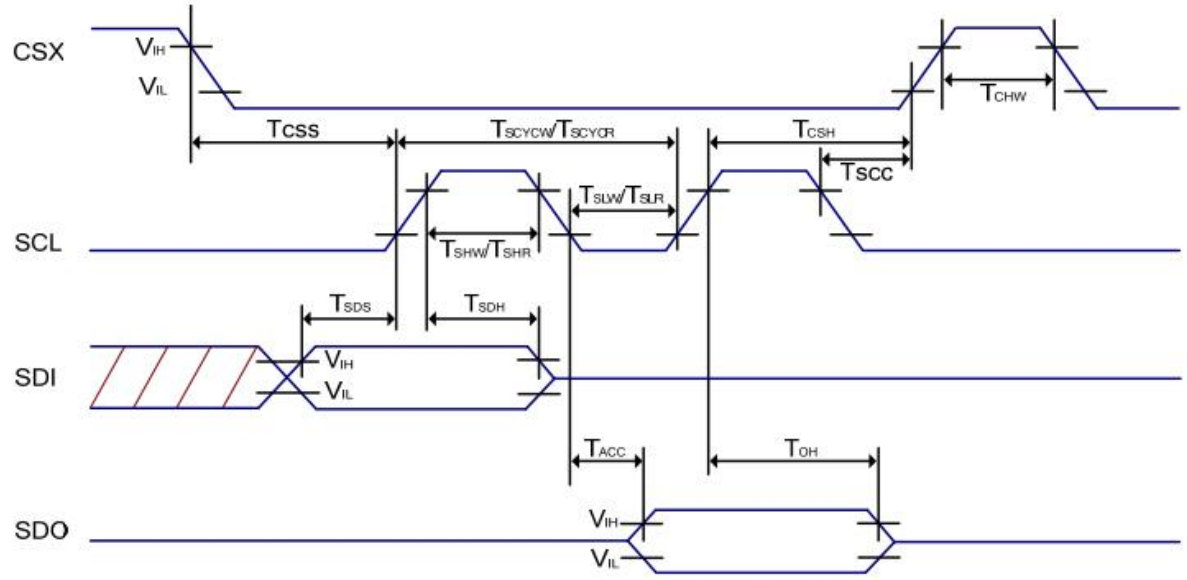


Table: SPI Interface Characteristics

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	Tcss	Chip select setup time	15	-	ns	-
	Tcsh	Chip select hold time	15	-	ns	
	Tscc	Chip select setup time	20	-	ns	
	Tchw	Chip “H” pulse width	40	-	ns	
SCL	Tscycw	Serial clock cycle (Write)	66	-	ns	-
	Tshw	SCL “H” pulse width (Write)	10	-	ns	
	Tslw	SCL “L” pulse width (Write)	10	-	ns	
	Tscycr	Serial clock cycle (Read)	150	-	ns	-
	Tshr	SCL“H” pulse width (Read)	60	-	ns	
	Tslr	SCL“L” pulse width (Read)	60	-	ns	
SDI	TSDS	Data setup time	10	-	ns	-
	Tsdh	Data hold time	10	-	ns	
	Tacc	Access time	10	50	ns	For maximum CL=30pF For minimum CL=8pF
	Toh	Output disable time	15	50	ns	

Note 1: IOVCC=1.65 to 3.6V, VCI=2.5 to 6V, VSSA=VSS=0V, Ta=-30 to 85℃

Note 2: The rise time and fall time (tr, tf) of input signal is specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

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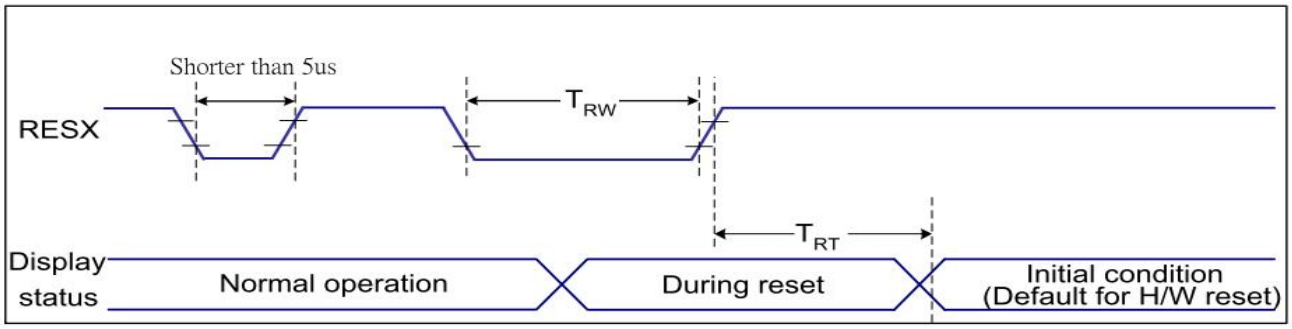
6.3. 复位时序 Reset Timing



VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25 ℃

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120(Note 1, 6, 7)	ms

注释 Notes:

1. 复位取消还包括将 ID 字节、VCOM 设置和其他设置从 NVM（或类似设备）加载到寄存器所需的时间。 每次在 RESX 上升沿后 5 ms 内有硬件复位取消时间（tRT）时，都会执行此加载。

The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

2. 根据下表，由于 RESX 线路上的静电放电引起的尖峰不会导致不规则的系统复位

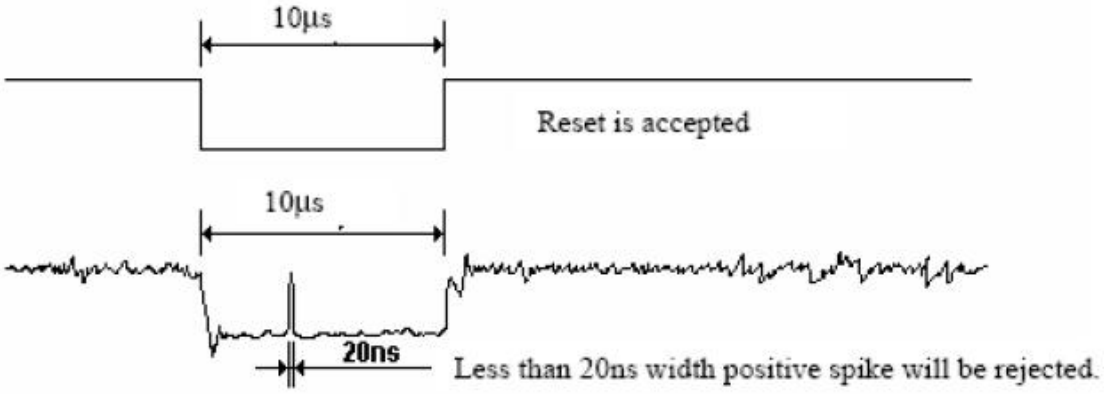
Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. 在复位期间，显示屏将被消隐（显示屏进入消隐序列，最长持续时间为 120 毫秒，此时 Reset Starts in Sleep Out －mode。显示屏在 Sleep In －mode 中保持消隐状态。）然后返回 硬件复位的默认条件。

4. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out －mode. The display remains the blank state in Sleep In －mode.) and then return to Default condition for Hardware Reset.

4. 尖峰抑制也适用于有效复位脉冲期间，如下所示：
   Spike Rejection also applies during a valid reset pulse as shown below:



5. 在进入睡眠期间应用复位时
   When Reset applied during Sleep In Mode.
6. 在退出睡眠期间应用复位时
   When Reset applied during Sleep Out Mode.
7. 释放 RESX 后需要等待 5 毫秒以上才能发送命令。也不能在 120 毫秒内发送睡眠退出命令。
   It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

6.4. 开机上电顺序 Power-on sequence

IOVCC 和 VCI 可以按任何顺序应用或断电。 在关机序列期间，如果 LCD 处于休眠模式，则 VCI 和 IOVCC 必须在至少 120 毫秒内断电。 如果 LCD 处于休眠模式，VCI 和 IOVCC 可以在 RESX 释放后至少 0 毫秒内断电。 CSX 可以在任何时间应用，也可以永久接地。 RESX 的优先级高于 CSX。

IOVCC and VCI can be applied or powered down in any order. During the Power Off sequence, if the LCD is in the Sleep Out mode, VCI and IOVCC must be powered down with minimum 120msec. If the LCD is in the Sleep In mode, VCI and IOVCC can be powered down with minimum 0msec after the RESX is released. CSX can be applied at any timing or can be permanently grounded. RESX has high priority over CSX.

备注 Notes：

1. 如果不满足电源顺序，TFT 不会损坏。

   TFT will not be damaged if the power sequence is not met.

2. 在电源开/关序列期间，显示面板上不会有异常的可见效果。

   There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.

3. 在 Power On Sequence 结束和收到 Sleep Out 命令之前，以及在收到 Sleep In 命令和 Power Off Sequence 之间，显示屏上不会出现异常可见的效果。

   There will be no abnormal visible effects on the display between the end of Power On Sequence and before receiving the Sleep Out command, and also between receiving the Sleep In command and the Power Off Sequence.

4. 如果在第 6.4.1 节中定义的开机序列期间主机没有稳定地保持 RESX 线，则有必要在主机开机序列完成后应用硬件复位（RESX）以确保 正确的操作。 否则，无法保证所有功能。

   If the RESX line is not steadily held by the host during the Power On Sequence as defined in Sections 6.4.1, then it will be necessary to apply the Hardware Reset (RESX) after the completion of the Host Power On Sequence to ensure correct operations. Otherwise, all the functions are not guaranteed.

6.4.1. 不受控制的断电 Uncontrolled Power Off

非受控断电是指在没有受控断电序列的情况下取出电池的情况。 它不会损坏模块或主机接口。

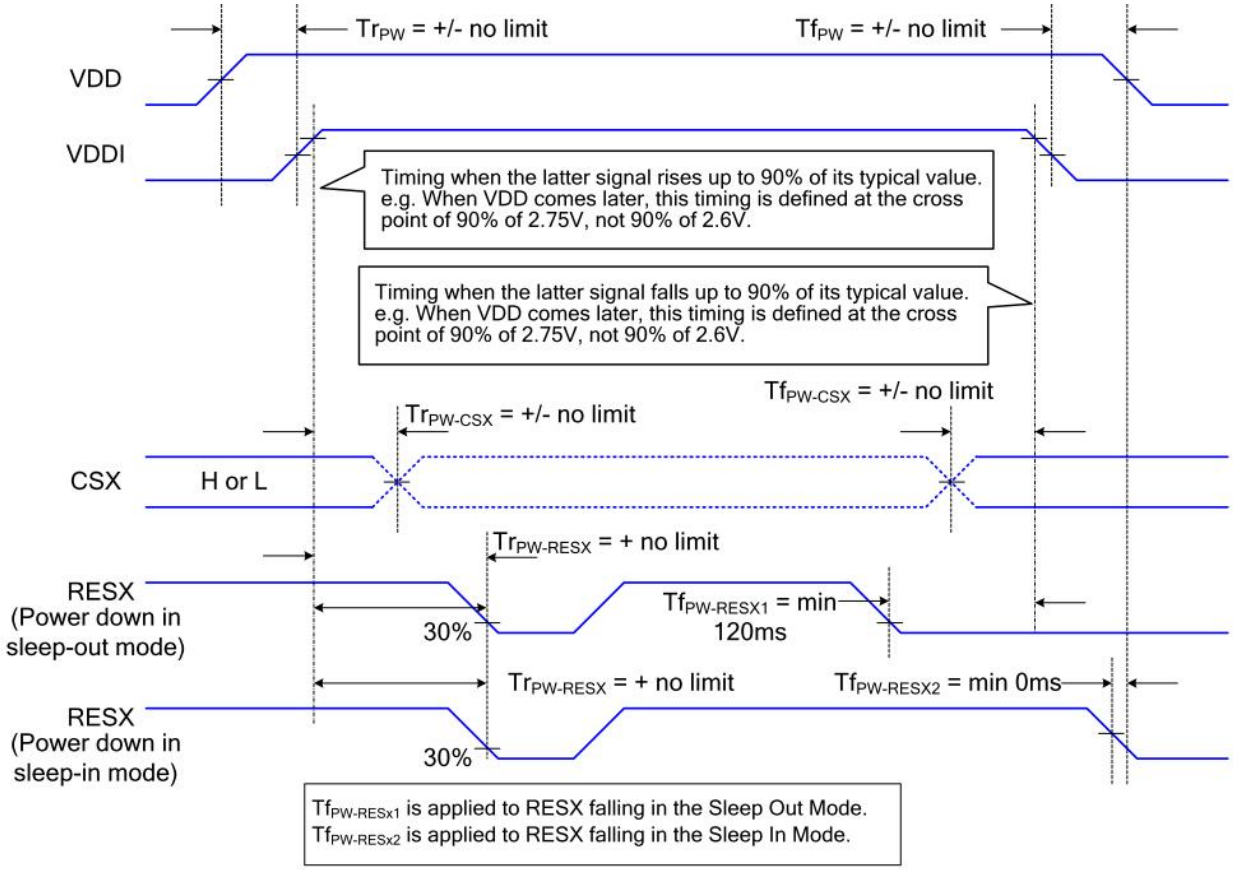
The uncontrolled power-off means a situation which removed a battery without the controlled power off sequence. It will neither damage the module or the host interface.

如果发生不受控制的断电，显示屏将变为空白，显示屏上不会有任何可见效果（空白显示）并保持空白，直到“Power On Sequence”启动它。

If uncontrolled power-off happened, the display will go blank and there will not any visible effect on the display (blank display) and remains blank until “Power On Sequence” powers it up.

电源开/关顺序如下图所示

The power on/off sequence is illustrated below



七、可靠性测试 RELIABILITY TEST

7-1 温度和湿度 Temperature and Humidity

测试项目 TEST ITEMS	条件 CONDITIONS	注释 NOTE
高温储存 High Temperature Storage	Ta=+80 ℃, 120hrs	
低温储存 Low Temperature Storage	Ta=-40 ℃, 120hrs	
高温运行试验 High Temperature Operation	Ta=+80 ℃, 120hrs	
低温运行试验 Low Temperature Operation	Ta=-30 ℃, 120hrs	
高温高湿（运行测试） High Temperature and High Humidity (Operating)	Ta=+60 ℃, 90%RH, 120hrs	

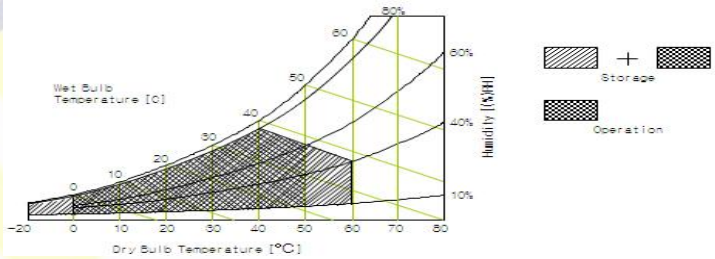
注释 Note:

1. 液晶驱动电压。由于液晶材料的特性，该电压随环境温度而变化。

Liquid Crystal driving voltage。Due to the characteristics of LC Material, this voltage varies with environmental temperature.

2. 温度和相对湿度范围如下图所示。湿球温度最高应为39℃。并且没有冷凝水。

Temperature and relative humidity range are shown in the figure below.Wet bulb temperature should be 39 ℃ max. and no condensation of water.



3. 产品经可靠性测试后，仅保证功能正常，无任何致命缺陷（不显示、线路缺陷、显示异常等）。

After the reliability test, the product only guarantee function normally without any fatal defect(non-display, line defect, abormal display etc).

4. 所有显示判断均在面板温度恢复到室温2小时后进行

All display judgments are made after the panel temperature returns to room temperature for 2 hours

5. Ta：环境温度

Ta：Ambient temperature

7-2 冲击和振动 Shock and Vibration

测试项目 TEST ITEMS	条件 CONDITIONS
包装冲击(非操作) Packing Shock (Non-Operation)	● Shock level:980m/s^2 ● Waveform:1/2 Sine wave,6msec ● ±X, ±Y ±Z,each axis 1 times
包装振动(非操作) Packing Vibration (Non-Operation)	● Frequency range:8-33.3HZ ● Stoke:1.0mm ● Sweep: 10Hz-50Hz ● x,y,z 2 hours for each direction

7-3 静电放电测试 Electrostatic Discharge

测试项目 TEST ITEMS	条件 CONDITIONS
ESD (Non-operation)	150pF,330Ω, Contact±4KV,Air :±8KV.Note 1
	200pF,0Ω, ±200V Contact test.Note 2

测量点 Measure Point:

1. LCD玻璃和金属边框
   LCD glass and metal bezel
2. 连接器引脚
   IF connector pins

八、处理和注意事项 HANDDLING & CAUTIONS

8-1 操作注意事项 Caution For Operation

♦由于液晶模组是玻璃材质，请勿对其施加强烈的机械冲击或静载荷。请小心搬运，因为冲击、振动和粗心的搬运可能会严重影响产品。如果从高处坠落或受到强烈冲击 ，玻璃可能碎了。

Since the LCM is made of glass, do not apply strong mechanical impact or static load onto it.Handling with care since shock, vibration, and careless handling may seriously affect the product.If it falls from a high place or receives a strong shock, the glass maybe broken.

♦在规定的电压限制内驱动 LCM 是必不可少的，因为高于限制的电压会导致 LCM 的寿命缩短。 由直流引起的电化学反应会导致 LCM 出现不良劣化，因此应避免使用直流驱动。

It is indispensable to drive the LCM within the specified voltage limit since the higher voltage than the limit causes LCM's life shorter. An electro-chemical reaction due to DC causes undesirable deterioration of the LCM so that the use of DC drive should avoid.

♦请勿在电源开启时将 LCM 连接到系统或从系统断开连接。

Do not connect or disconnect the LCM to or from the system when power is on.

♦切勿在高温高湿的异常条件下使用 LCM。

Never use the LCM under abnormal conditions of high temperature and high humidity.

♦当暴露于剧烈的温度波动（热到冷或冷到热）时，LCM可能会受到影响； 具体来说，从冷到热的剧烈温度波动会在 LCM 表面产生露水，这可能会影响 LCM 上偏振片的运行。

When expose to drastic fluctuation of temperature(hot to cold or cold to hot), the LCM may be affected; specifically, drastic temperature fluctuation from cold to hot, produces dew on the LCM's surface which may affect the operation of the polarizer on the LCM.

♦在低于工作温度范围的温度下，响应时间将极度延迟，另一方面，在高于其工作范围的温度下，LCM 可能会变黑。 然而，这些现象并不意味着 LCM 出现故障或故障。 一旦温度恢复到正常运行的推荐温度范围，LCM 将恢复正常运行。

Response time will be extremely delay at lower temperature than the operating temperature range and on the other hand LCM may turn black at temperature above its operational range. However those phenomenon do not mean malfunction or out of order with the LCM. The LCM will revert to normal operation once the temperature returns to the recommended temperature range for normal operation.

♦为防止LCD产生图像残留，在使用常黑面板时不要长时间显示固定图案。 如果LCD需要显示固定图案，建议时间少于 2 分钟或者更短的时间内刷新显示内容一次或多次。强烈建议使用黑色图像或移动图像作为屏幕保护程序。

In order to prevent the LCD from producing image retention, do not display a fixed pattern for a long time when using a normally black panel. If the LCD needs to display a fixed pattern, it is recommended to refresh the display one or more times in less

than 2 minutes or less. It is strongly recommended to use a black image or moving image as a screen saver.

♦强烈的光照会导致 LCD 偏光片和彩色滤光片退化。 不准在强光或高温高湿下长期存放或直接运行。

Strong sunlight can cause LCD polarizers and color filters to degrade. Long-term storage or direct operation under strong light or high temperature and humidity is not allowed.

8-2 防静电措施 Caution Against Static Charge

♦LCM 使用 C-MOS LSI 驱动器，因此建议客户将任何未使用的输入端连接到 Vdd 或 Vss，上电前不要输入任何信号，并将您的身体、工作/装配区、装配设备接地 防止静电。

The LCM use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.

♦缓慢去除保护膜，保持去除方向与面板表面不垂直约 30 度，如有可能，在离子风机等 ESD 控制装置下，工作室内湿度应保持在 50%RH 以上，以减少静电风险

Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, if possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.

♦避免使用合成纤维制成的工作服。 我们推荐棉质衣服或其他经过导电处理的纤维。

Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

♦在处理 LCM 时，请戴上不带电材料的手套。 对地导电手腕和对地导电鞋是必需的

In handling the LCM, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary

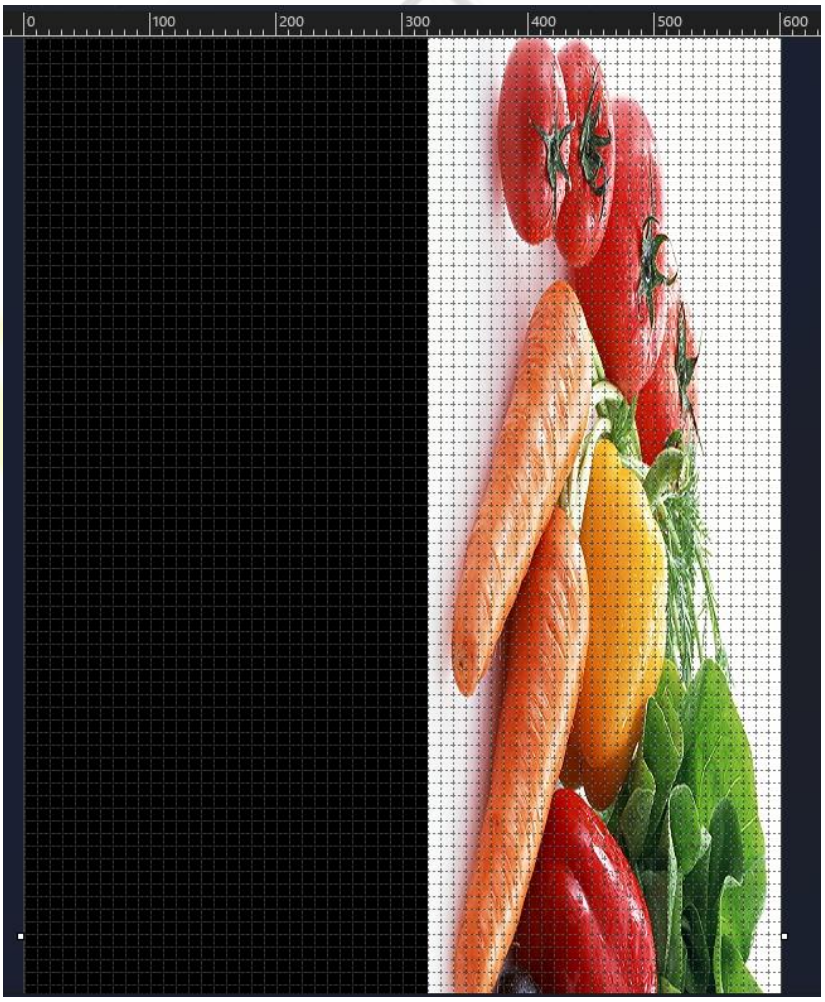
九、初始化代码 Initialization code

```
Void Panel_Initial_code(void)
{
//Power:VCI=3.3
//24位配置 24 bits configuration
#define HSPW 2
#define HBPD 30
#define HFPD 24

#define VSPW 2
#define VBPD 8
#define VFPD 16

#HDP 600
#VDP 1424
//图片需要黑色数据处理作为补充，实际上仅显示280x1424
```

The image requires black data processing as a supplement and actually only displays 280x1424



```
//================上电复位操作===============//
LCD_RESET=1;
Delayms(1);          //Delay 1ms
LCD_RESET=0;
Delayms(10);          //Delay 10ms
LCD_RESET=1;
Delayms(120);         //Delay 120ms
//=============================================//
Wrt_Reg_3052(0xFF,0x30);
Wrt_Reg_3052(0xFF,0x52);
Wrt_Reg_3052(0xFF,0x01);
Wrt_Reg_3052(0xE3,0x00);
Wrt_Reg_3052(0x08,0x00);
Wrt_Reg_3052(0x09,0x07);
Wrt_Reg_3052(0x0A,0xfd);
Wrt_Reg_3052(0x0B,0x32);
Wrt_Reg_3052(0x0C,0x32);
Wrt_Reg_3052(0x0D,0x0B);
Wrt_Reg_3052(0x0E,0x00);
Wrt_Reg_3052(0x24,0x10);
Wrt_Reg_3052(0x25,0x0a);
Wrt_Reg_3052(0x20,0xA0);
Wrt_Reg_3052(0x28,0x50);
Wrt_Reg_3052(0x29,0xc5);
Wrt_Reg_3052(0x2a,0x90);
Wrt_Reg_3052(0x38,0x9C);
Wrt_Reg_3052(0x39,0xA7);
Wrt_Reg_3052(0x3A,0x37);
Wrt_Reg_3052(0x49,0x3C);
Wrt_Reg_3052(0x6D,0x00);
Wrt_Reg_3052(0x6E,0x00);
Wrt_Reg_3052(0x80,0x20);
Wrt_Reg_3052(0x91,0x67);
Wrt_Reg_3052(0x92,0x67);
Wrt_Reg_3052(0x99,0x51);
Wrt_Reg_3052(0x9b,0x59);
Wrt_Reg_3052(0xA0,0x55);
Wrt_Reg_3052(0xA1,0x50);
Wrt_Reg_3052(0xA3,0xD8);
Wrt_Reg_3052(0xA4,0x9C);
```

```
Wrt_Reg_3052(0xA7,0x02);
Wrt_Reg_3052(0xA8,0x01);
Wrt_Reg_3052(0xA9,0x01);
Wrt_Reg_3052(0xAA,0xA8);
Wrt_Reg_3052(0xAB,0x28);
Wrt_Reg_3052(0xAC,0xE0);
Wrt_Reg_3052(0xAD,0xE2);
Wrt_Reg_3052(0xAE,0xE2);
Wrt_Reg_3052(0xAF,0x02);
Wrt_Reg_3052(0xB0,0xE2);
Wrt_Reg_3052(0xB1,0x26);
Wrt_Reg_3052(0xB2,0x28);
Wrt_Reg_3052(0xB3,0x28);
Wrt_Reg_3052(0xB4,0x22);
Wrt_Reg_3052(0xB5,0xE2);
Wrt_Reg_3052(0xB6,0x26);
Wrt_Reg_3052(0xB7,0xE2);
Wrt_Reg_3052(0xB8,0x26);

Wrt_Reg_3052(0xF6,0xC0);
Wrt_Reg_3052(0xF0,0x00);

Wrt_Reg_3052(0xFF,0x30);
Wrt_Reg_3052(0xFF,0x52);
Wrt_Reg_3052(0xFF,0x02);
Wrt_Reg_3052(0xB1,0x0B);
Wrt_Reg_3052(0xD1,0x0B);
Wrt_Reg_3052(0xB4,0x31);
Wrt_Reg_3052(0xD4,0x2F);
Wrt_Reg_3052(0xB2,0x09);
Wrt_Reg_3052(0xD2,0x09);
Wrt_Reg_3052(0xB3,0x2F);
Wrt_Reg_3052(0xD3,0x2D);
Wrt_Reg_3052(0xB6,0x14);
Wrt_Reg_3052(0xD6,0x13);
Wrt_Reg_3052(0xB7,0x37);
Wrt_Reg_3052(0xD7,0x36);
Wrt_Reg_3052(0xC1,0x08);
Wrt_Reg_3052(0xE1,0x07);
Wrt_Reg_3052(0xB8,0x0C);
```

```
Wrt_Reg_3052(0xD8,0x0C);
Wrt_Reg_3052(0xB9,0x03);
Wrt_Reg_3052(0xD9,0x02);
Wrt_Reg_3052(0xBD,0x13);
Wrt_Reg_3052(0xDD,0x13);
Wrt_Reg_3052(0xBC,0x11);
Wrt_Reg_3052(0xDC,0x11);
Wrt_Reg_3052(0xBB,0x10);
Wrt_Reg_3052(0xDB,0x0F);
Wrt_Reg_3052(0xBA,0x10);
Wrt_Reg_3052(0xDA,0x10);
Wrt_Reg_3052(0xBE,0x18);
Wrt_Reg_3052(0xDE,0x1A);
Wrt_Reg_3052(0xBF,0x0F);
Wrt_Reg_3052(0xDF,0x11);
Wrt_Reg_3052(0xC0,0x16);
Wrt_Reg_3052(0xE0,0x18);
Wrt_Reg_3052(0xB5,0x37);
Wrt_Reg_3052(0xD5,0x32);
Wrt_Reg_3052(0xB0,0x02);
Wrt_Reg_3052(0xD0,0x05);
Wrt_Reg_3052(0xFF,0x30);
Wrt_Reg_3052(0xFF,0x52);
Wrt_Reg_3052(0xFF,0x03);
Wrt_Reg_3052(0x04,0x51);
Wrt_Reg_3052(0x05,0x50);
Wrt_Reg_3052(0x06,0x50);
Wrt_Reg_3052(0x07,0x03);
Wrt_Reg_3052(0x08,0x04);
Wrt_Reg_3052(0x09,0x05);
Wrt_Reg_3052(0x0A,0x06);
Wrt_Reg_3052(0x0B,0x07);
Wrt_Reg_3052(0x24,0x51);
Wrt_Reg_3052(0x25,0x50);
Wrt_Reg_3052(0x26,0x50);
Wrt_Reg_3052(0x27,0x03);
Wrt_Reg_3052(0x28,0x55);
Wrt_Reg_3052(0x29,0x55);
Wrt_Reg_3052(0x2A,0xa7);
Wrt_Reg_3052(0x2B,0xab);
```

```
Wrt_Reg_3052(0x2c,0xab);
Wrt_Reg_3052(0x34,0x51);
Wrt_Reg_3052(0x35,0x50);
Wrt_Reg_3052(0x36,0x50);
Wrt_Reg_3052(0x37,0x03);
Wrt_Reg_3052(0x40,0x04);
Wrt_Reg_3052(0x41,0x05);
Wrt_Reg_3052(0x42,0x06);
Wrt_Reg_3052(0x43,0x07);
Wrt_Reg_3052(0x44,0x55);
Wrt_Reg_3052(0x45,0x9f);
Wrt_Reg_3052(0x46,0xa0);
Wrt_Reg_3052(0x47,0x55);
Wrt_Reg_3052(0x48,0xa1);
Wrt_Reg_3052(0x49,0xa2);
Wrt_Reg_3052(0x50,0x08);
Wrt_Reg_3052(0x51,0x09);
Wrt_Reg_3052(0x52,0x0a);
Wrt_Reg_3052(0x53,0x0b);
Wrt_Reg_3052(0x54,0x55);
Wrt_Reg_3052(0x55,0xa3);
Wrt_Reg_3052(0x56,0xa4);
Wrt_Reg_3052(0x57,0x55);
Wrt_Reg_3052(0x58,0xa5);
Wrt_Reg_3052(0x59,0xa6);
Wrt_Reg_3052(0x60,0x03);
Wrt_Reg_3052(0x61,0x03);
Wrt_Reg_3052(0x64,0x00);
Wrt_Reg_3052(0x65,0x0a);
Wrt_Reg_3052(0x66,0x0a);
Wrt_Reg_3052(0x80,0x06);
Wrt_Reg_3052(0x81,0x11);
Wrt_Reg_3052(0x82,0x07);
Wrt_Reg_3052(0x83,0x03);
Wrt_Reg_3052(0x84,0x1f);
Wrt_Reg_3052(0x85,0x1e);
Wrt_Reg_3052(0x86,0x01);
Wrt_Reg_3052(0x87,0x1e);
Wrt_Reg_3052(0x88,0x09);
Wrt_Reg_3052(0x89,0x0b);
```

```
Wrt_Reg_3052(0x8A,0x0d);
Wrt_Reg_3052(0x8B,0x0f);
Wrt_Reg_3052(0x96,0x06);
Wrt_Reg_3052(0x97,0x11);
Wrt_Reg_3052(0x98,0x07);
Wrt_Reg_3052(0x99,0x04);
Wrt_Reg_3052(0x9a,0x1f);
Wrt_Reg_3052(0x9b,0x1e);
Wrt_Reg_3052(0x9c,0x02);
Wrt_Reg_3052(0x9d,0x1e);
Wrt_Reg_3052(0x9e,0x0a);
Wrt_Reg_3052(0x9f,0x0c);
Wrt_Reg_3052(0xA0,0x0e);
Wrt_Reg_3052(0xA1,0x10);
Wrt_Reg_3052(0xFF,0x30);
Wrt_Reg_3052(0xFF,0x52);
Wrt_Reg_3052(0xFF,0x02);
Wrt_Reg_3052(0x28,0x0B);
Wrt_Reg_3052(0x29,0x07);
Wrt_Reg_3052(0x2A,0x81);
Wrt_Reg_3052(0x01,0x01);
Wrt_Reg_3052(0x02,0xDA);
Wrt_Reg_3052(0x03,0xBA);
Wrt_Reg_3052(0x04,0xA8);
Wrt_Reg_3052(0x05,0x9A);
Wrt_Reg_3052(0x06,0x70);
Wrt_Reg_3052(0x07,0xFF);
Wrt_Reg_3052(0x08,0x91);
Wrt_Reg_3052(0x09,0x90);
Wrt_Reg_3052(0x0A,0xFF);
Wrt_Reg_3052(0x0B,0x8F);
Wrt_Reg_3052(0x0C,0x60);
Wrt_Reg_3052(0x0D,0x58);
Wrt_Reg_3052(0x0E,0x48);
Wrt_Reg_3052(0x0F,0x38);
Wrt_Reg_3052(0x10,0x2B);
Wrt_Reg_3052(0xFF,0x30);
Wrt_Reg_3052(0xFF,0x52);
Wrt_Reg_3052(0xFF,0x00);
Wrt_Reg_3052(0x36,0x0A);
```

```
Wrt_Reg_3052(0x11,0x00);
Wrt_Reg_3052(0x11);
Delay_ms(200);
Wrt_Reg_3052(0x29);
Delay_ms(100);
}
```

文档修订记录 Document revision history ：

版本 Version	日期 DATE	修改说明 Modify description	编著 Editorial
0-0	2024-01-19	初次编制 First compilation.	YL

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－－ END －－

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