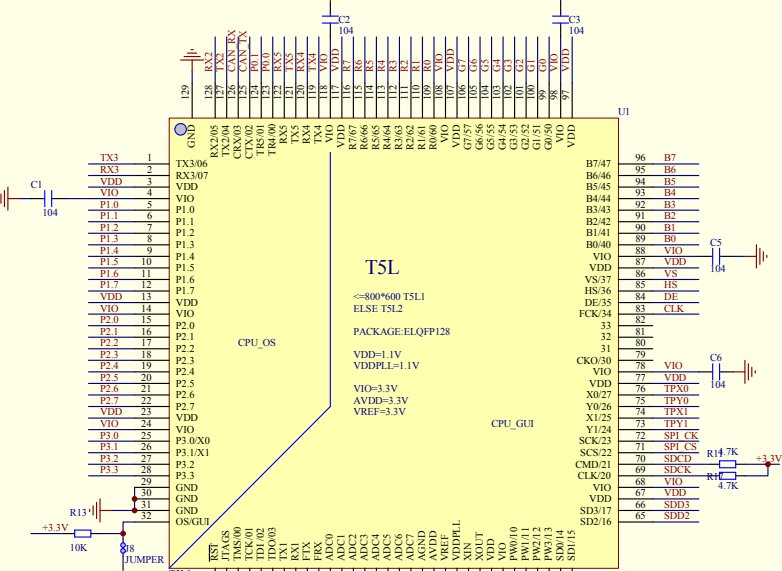
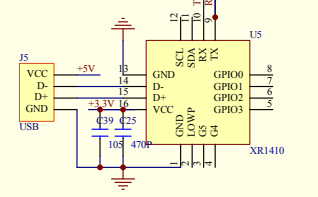


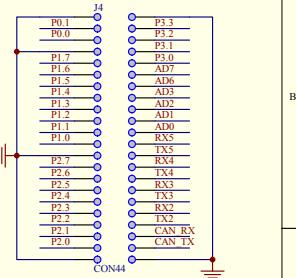
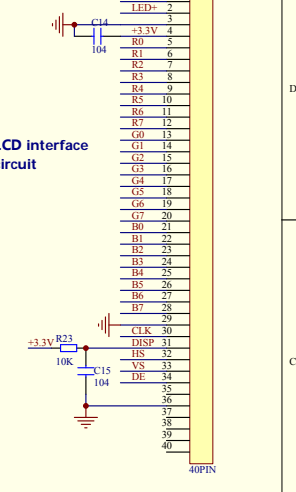
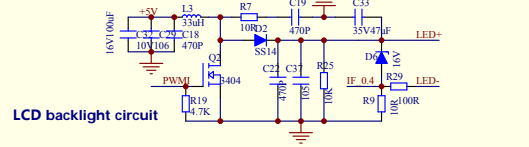
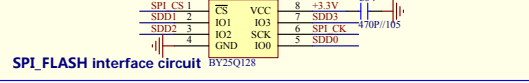
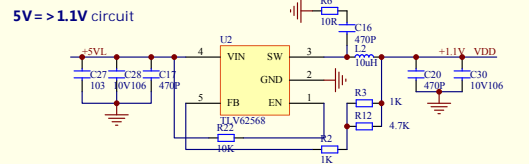
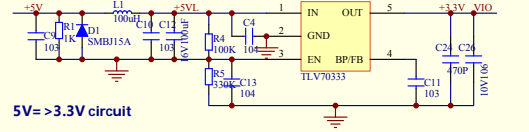
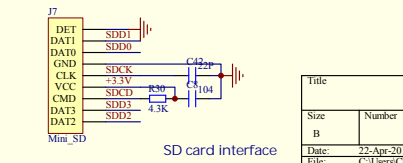
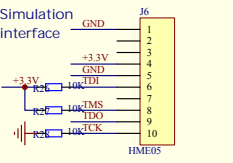
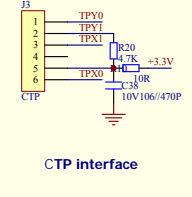
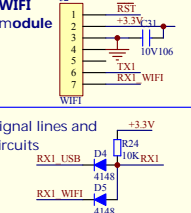
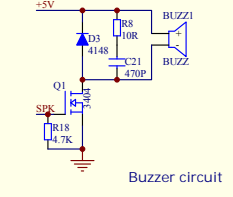
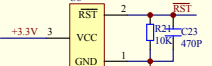
T5L master chip



USB debug interface



Reset circuit



Title		
Size	Number	Revision
B		
Date:	22-Apr-2019	Sheet of
File:	C:\Users\ChinaRen\Desktop\T5LEK4315	Drawn By